

PATENT Customer No. 22,852 Attorney Docket No. 08245.0027

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Jun Dong KIM et al.) Group Art Unit: 2814
Application No.: 09/892,878	Examiner: Shrinivas H. Rao
Filed: June 28, 2001	RECE DEC 10 2800
For: METHOD FOR FORMING GATE ELECTRODES IN A SEMICONDUCTOR DEVICE USING FORMED FINE PATTERNS	CEIVED ON MAIL ROOM ON MAIL ROOM

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

AMENDMENT

In reply to the Office Action mailed July 28, 2003, the period for response having been extended to December 28, 2003 by a request for extension of two months and fee payment filed concurrently herewith, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims in this paper.

Remarks follow the amendment sections of this paper.

FINNEGAN HENDERSON FARABOW GARRETT & DUNNERLL

1300 I Street, NW Washington, DC 20005 202.408.4000 Fax 202.408.4400 www.finnegan.com